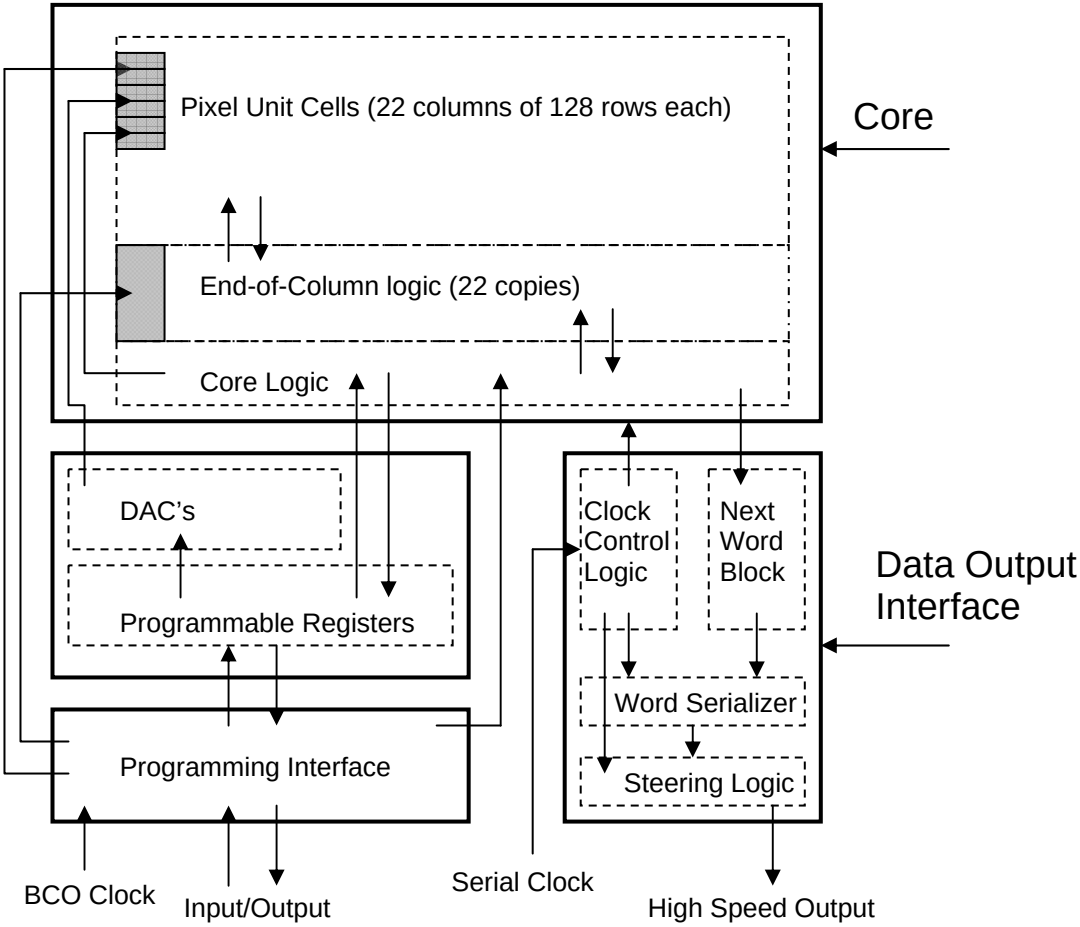
Pixel Unit Cells (22 columns of 128 rows each)
Core
End-of-Column logic (22 copies)
Core Logic
DAC's
Programmable Registers
Clock Control Logic
Next Word Block
Data Output Interface
Word Serializer
Steering Logic
Programming Interface
BCO Clock
Input/Output
Serial Clock
High Speed Output